

## U.S. UTILITY Patent Application

**O.J.P.E.**

PATENT DATE

SCANNED

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APPLICATION NO. 09/922671	CONT/PRIOR F	CLASS 365 257	SUBCLASS 708	ART UNIT 2818 2815	EXAMINER Wanan
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CMOS basic cell and method for fabricating semiconductor integrated circuit using the same

ISSUING CLASSIFICATION								
ORIGINAL				CROSS REFERENCE(S)				
CLASS		SUBCLASS	CLASS	SUBCLAS (ONE SUBCLASS PER BLOCK)				
				<input checked="" type="checkbox"/>				
INTERNATIONAL CLASSIFICATION:								

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<b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	<b>DRAWINGS</b> Sheets Drawg.    Figs. Drawg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>   <b>ISSUE FEE</b> Amount Due    Date Paid	
_____ (Primary Examiner)    (Date)					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)    (Date)			<b>ISSUE BATCH NUMBER</b>	

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